

Product Brief

15 to 1100 MHz 6-Channel Clock Synthesizer **APCLK6**



The APCLK 6 is a low phase-noise clock synthesizer operating up to 1100 MHz.

Up to six differential clocks (PECL) with programmable amplitude and fast rise/fall times can be generated. The module has a frequency resolution of less than a milli-Hz and can be locked to an external reference.

The module has a simple SPI programming interface and allows fast switching of frequency and output amplitude. Operating from 5V and 3.3V supplies, the clock module consumes less than three watts.

Key Features

- Total jitter typically < 100 fs
- Fully differential outputs
- Works with external low noise reference
- Typically 60 ps rise/fall times
- Programmable output amplitudes
- SPI-controlled
- 5V / 3.3V supply , 3 watts

• Applications

- ADC clock source
- OEM module